WEST Search History

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DATE: Monday, November 14, 2005

Hide?	<u>Set</u> Name	Query	<u>Hit</u> Count		
DB = PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR = YES; OP = ADJ					
	L32	('6678850')!.ABPN1,NRPN,PN,TBAN,WKU.	2		
	L31	129 and conner and parallel near test\$3 same memory	12		
	L30	129 and conner and parallel near test\$3	41		
	L29	conner and parallel	7802		
	L28	(single or 'only' or unique) with(resume or hold)with (pin or terminal or input or port) same MEMORY with (BIST or (automat\$4 or self\$1 or built\$4) near2 test\$3 or CONTROLLER) same address\$3 same integrated near circuit\$3.clm.	0		
	L27	L25 not 121	9		
	L26	L25 not 121	9		
	L25	(single or 'only' or unique) with(resume or hold)with (pin or terminal or input or port) same MEMORY with (BIST or (automat\$4 or self\$1 or built\$4) near2 test\$3 or CONTROLLER)and address\$3 and integrated near circuit\$3.clm.	10		
	L24	4528626	30		
	L23	('5448742')!.ABPN1,NRPN,PN,TBAN,WKU.	2		
	L22	('20010054116')!.ABPN1,NRPN,PN,TBAN,WKU.	2		
	L21	(single or 'only' or unique) with(resume or hold)with (pin or terminal or input or port) same MEMORY with (BIST or (automat\$4 or self\$1 or built\$4) near2 test\$3 or CONTROLLER).clm.	5		
	L20	(single or 'only' or unique) with(resume or hold)with (pin or terminal or input) same MEMORY with (BIST or (automat\$4 or self\$1 or built\$4) near2 test\$3 or CONTROLLER).clm.	4		
	L19	('20040014485')!.ABPN1,NRPN,PN,TBAN,WKU.	2		
	L18	(single or 'only' or unique) with(resume or hold)with (pin or terminal) same MEMORY with (BIST or CONTROLLER).clm.	1		
	L17	(single or 'only' or unique) with(resume or hold)with (pin or teminal) same MEMORY with (BIST or CONTROLLER).clm.	0		
	L16	L15 and @pd > 20050725	0		
· 🗀	L15	09872212	0		
	L14	('6347056' '6760865' '20020174382' '20020174394')!.PN.	7		
	L13	L7 and (single or 'only' or unique) with(resume or hold)with (pin or teminal) same MEMORY with (BIST or CONTROLLER)	4		
	L12	L11 and @pd > 20041226	1		
	L11	L10 not L9	3		

L10	(resum\$5 or hold\$3 or continu\$4)near2 (pin or terminal or pad or lead or port) same MEMORY with BIST with CONTROL\$4	7
L9	L8 and (resume or hold)near2 (pin or teminal) same MEMORY with BIST with CONTROL\$4	4
L8	L7 and (resume or hold)with (pin or teminal) same MEMORY with (BIST or CONTROLLER)	86
L7	(resume or hold)with (pin or teminal) and MEMORY with (BIST or CONTROLLER)	990
L6	DIFFERENT MEMORY with BIST CONTROLLERS	2
L5	DIFFERENT MEMORY BIST CONTROLLERS	0
L4	SYNCHRONIZATION POINT ACROSS DIFFERENT MEMORY BIST CONTROLLERS	0
L3	SYNCHRONIZATION with DIFFERENT MEMORY with BIST CONTROLLERS.ti.	0
L2	SYNCHRONIZATION POINT ACROSS DIFFERENT MEMORY BIST CONTROLLERS.ti.	0
L1	9872212.pn.	1

END OF SEARCH HISTORY